**Work Flow**

1. **Enable GPIO clock and configure I/O as input line.**
2. **Enable SYSCFG clock (RCC\_APB2ENR) & configure external interrupt line mapping in SYSCFG\_EXTICR**
3. **Unmask the interrupt line in EXTI\_IMR**
4. **Configure the trigger selection bits (Rising/falling) of the interrupt line EXTI\_RTSR, EXTI\_FTSR**
5. **Configure and enable the interrupt line in NVIC (NVIC\_ISER)**
6. **When the interrupt is triggered clear pending bit by programming it to 1 in EXT1\_PR**